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| APPLICATION NO. 05/648,484 | FILING DATE 05/05/96 | FIRST NAMED INVENTOR MOON | ATTORNEY DOCKET NO. 27951-00023 |
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B3M1/0602

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| EXAMINER THILANG, E |
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| ART UNIT 2385 | PAPER NUMBER 2 |
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DATE MAILED: 06/02/97

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.

08/646,484

Applicant(s)

Moon, Billy G.

Examiner

Eric S. Thlang.

Group Art Unit

2305



- ☐ Responsive to communication(s) filed on _____.
- ☐ This action is **FINAL**.
- ☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claims

- ☒ Claim(s) 1-10 is/are pending in the application.
- Of the above, claim(s) _____ is/are withdrawn from consideration.
- ☐ Claim(s) _____ is/are allowed.
- ☒ Claim(s) 1-10 is/are rejected.
- ☐ Claim(s) _____ is/are objected to.
- ☐ Claims _____ are subject to restriction or election requirement.

Application Papers

- ☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.
- ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- ☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.
- ☐ The specification is objected to by the Examiner.
- ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- ☐ All ☐ Some* ☐ None of the CERTIFIED copies of the priority documents have been
- ☐ received.
- ☐ received in Application No. (Series Code/Serial Number) _____.
- ☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____

- ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- ☒ Notice of References Cited, PTO-892
- ☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____
- ☐ Interview Summary, PTO-413
- ☐ Notice of Draftsperson's Patent Drawing Review, PTO-948
- ☐ Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

Part III DETAILED ACTION

Notice to Applicant(s)

1. Claims 1-10 are presented for examination.
2. The Group and/or Art Unit location of your application in the PTO has changed. To aid in correlating any papers for this application, all further correspondence regarding this application should be directed to Group Art Unit 2305.

Informalities - Objections

3. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Drawings

4. This application has been filed with informal drawings which are acceptable for examination purposes only. Formal drawings will be required when the application is allowed.
5. Direct any inquires concerning drawing review to the Drawing Review Branch at (703) 305 - 8404.

Claim Rejections - 35 U.S.C. § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

7. Claims 1-10 rejected under 35 U.S.C. § 102(b) as being anticipated by Paneth et al. (U.S. 4,817,089).

As per claims 1, 3, and 5, Paneth (4,817,089) teaches:

(a) an input for receiving a half duplex mode enable signal, which half duplex mode enable signal causes said UART to operate in a half duplex mode of operation (col. 9, lines 57-68) and in a full duplex of operation (col. 42, lines 4-18);

(b) at least one input for receiving at least one signal indicating whether there is data for the UART to transmit (col. 59, lines 62-68); and

© circuitry for disabling a receiver port of said UART, which circuitry is coupled to said input for receiving said half duplex mode enable signal, which circuitry is also coupled to said at least one input for receiving at least one signal, and which circuitry also has an output coupled to said receiver port of said UART to enable said receiver port to receive (col. 34, lines 28-31).

As per claim 2, Paneth teaches all claimed limitations as applied to claims 1, 3, 5 above. Furthermore, Paneth teaches an input from a FIFO register set for carrying a FIFO empty signal received indicating that there is no data in the FIFO register set for transmission by the UART (Fig. 12, #59; col. 61, lines 35-67).

As per claims 4, Paneth teaches all claimed limitations as applied to claims above. Furthermore, Paneth teaches an input from a data store for carrying a data

store empty signal indicating that there is not data within said data store for transmission by the UART (col. 61, lines 35-54).

As per claims 6-7, and 9-10, Paneth teaches all claimed limitations as applied to claims above. Furthermore, Paneth teaches further an input from a processing unit (col. 2, lines 4-68) indicating whether a UART receiver enable flag has been set wherein said UART control circuit (col. 62, lines 12-18) output only enables said receiver whenever said input from said processing unit indicating that said UART receiver enable flag has been set (col. 51, lines 1-4) and (col. 68, lines 27-64).

As per claims 8, Paneth teaches all claimed limitations as applied to claims above. Furthermore, Paneth teaches further includes an input for receiving a half duplex mode enable signal, which input is coupled to an external device (see abstract).

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

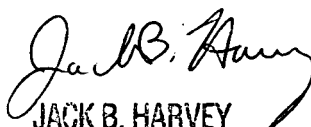
Curry (5,045,675) teaches UART chip in the computer's RS232 interface (see abstract)

Tanenbaum (5,119,319) teaches full-duplex mode operation (see title).

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Eric S. Thlang, whose telephone number is (703) 305-4004. The examiner can normally be reached on Monday-Thursday from 8:00AM- 5:30PM. The examiner can also be reached on alternate Fridays during the same hours.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Harvey, can be reached on (703) 305-9705. The fax phone number for this Group is (703) 308-5358.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-3900.


JACK B. HARVEY
SUPERVISORY PATENT EXAMINER
GROUP 2300

EST
May 17, 1997